

The EU Automotive Action Plan: Implementation elements under the Chips JU

CNECT MT

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Automotive Action plan (AAP) and Strategic Dialogue

Digital topics feature heavily in the Strategic Dialogue and Automotive Action Plan



Ursula von der Leyen
President



Jan 30 – Kick-off
Mar 3 – Closing with Pres VdL



Henna Virkkunen
EVP



Stéphane Séjourné
EVP



Roxana Mînzatu
EVP



Wopke Hoekstra
Commissioner



Apostolos Tzitzikostas
Commissioner

Dialogues with 5 other EVPs/Commissioners

Feb 17, Strategic Dialogue with EVP Virkkunen



26 participants, 4 Focus topics

- 1 Software-Defined Vehicles (SDVs) for connected, automomous, and electric mobility
- 2 Automotive Hardware and critical technologies
- 3 Data Access and Pooling
- 4 Regulatory simplification



Hand-over of the “**Declaration to collaborate on an EU Software-Defined Vehicle**” signed by 15 OEMs, Tier 1 suppliers, and DE, FR, IT Automotive associations



March 5 – Publication of the
Action Plan for Automotive

The outcome of this dialogue is a
comprehensive Action Plan

5 Chapters:

Innovation and Digitalisation

Decarbonisation

Competitiveness and supply resilience

Skills and Social dimension

Level playing field and business environ.

AAP: Chapter on Innovation and Digitalisation

Actions to regain a leadership position in Software-enabled, AI powered, Connected and Autonomous vehicles

High relevance for Digital Vehicle

Detailed next

Digital Vehicle	Data	Batteries	Regulatory framework
 <p>European Connected and Autonomous Vehicle Alliance</p> <p>Common architecture elements, shared European hardware and software building blocks as well as standardisation</p>	 <p>Vehicle data, functions, resources</p> <p>Measures to allow the full automotive ecosystem to reap the benefits of data from connected vehicles, while taking into account cybersecurity</p>	 <p>Next-Gen battery Tech</p> <p>Support the whole EU value chain of next generation batteries, including recycling, and partnerships in advanced manufacturing and advanced materials</p>	 <p>Regulatory cross-border test-beds</p> <p>At least three large-scale cross-border testbeds, related regulatory sandboxes and European Automated Driving Corridors to allow for at-scale pilot deployment of AVs</p>
 <p>Cybersecurity</p> <p>Follow up on an on-going cybersecurity risk assessment on connected vehicles</p>	 <p>Competition</p> <p>Assessment whether the existing EU competition framework on vertical agreements in aftermarkets is still fit for purpose</p>		 <p>Harmonised single market on AVs</p> <ul style="list-style-type: none"> i) Rules on automated driving systems by 2026; ii) Harmonised rules for testing of ADAS and ADS on public roads by 2026 iii) Harmonised deployment rules across EU

Funding



2025-27 EUR 1 bn
(joint public & private investment: Chips JU & HE partnerships)

2025-2027 EUR 362 Mn
(HE Batt4EU partnership)

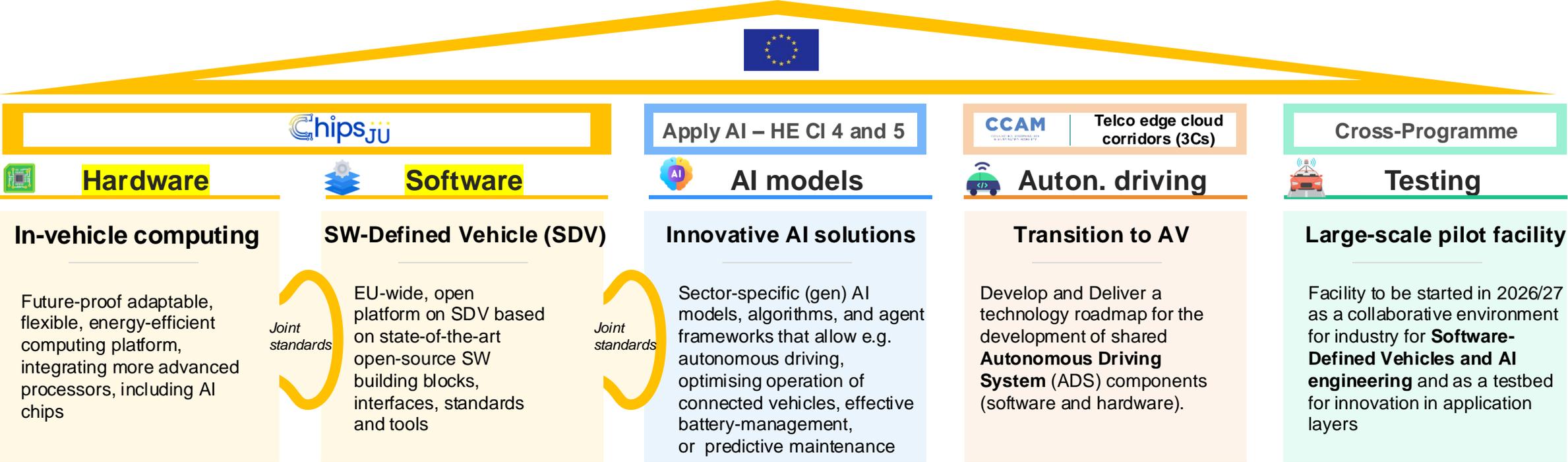
Option for a Joint Undertaking for Automotive under FP 10 – 2028 ++

Catalysing private investments for innovation under the Clean Industrial Deal
(InvestEU for Clean Tech and Clean Mobility)

Future IPCEI by Member States
(Clean, Connected, Autonomous Vehicle – DE, FR, SL and IT leading)



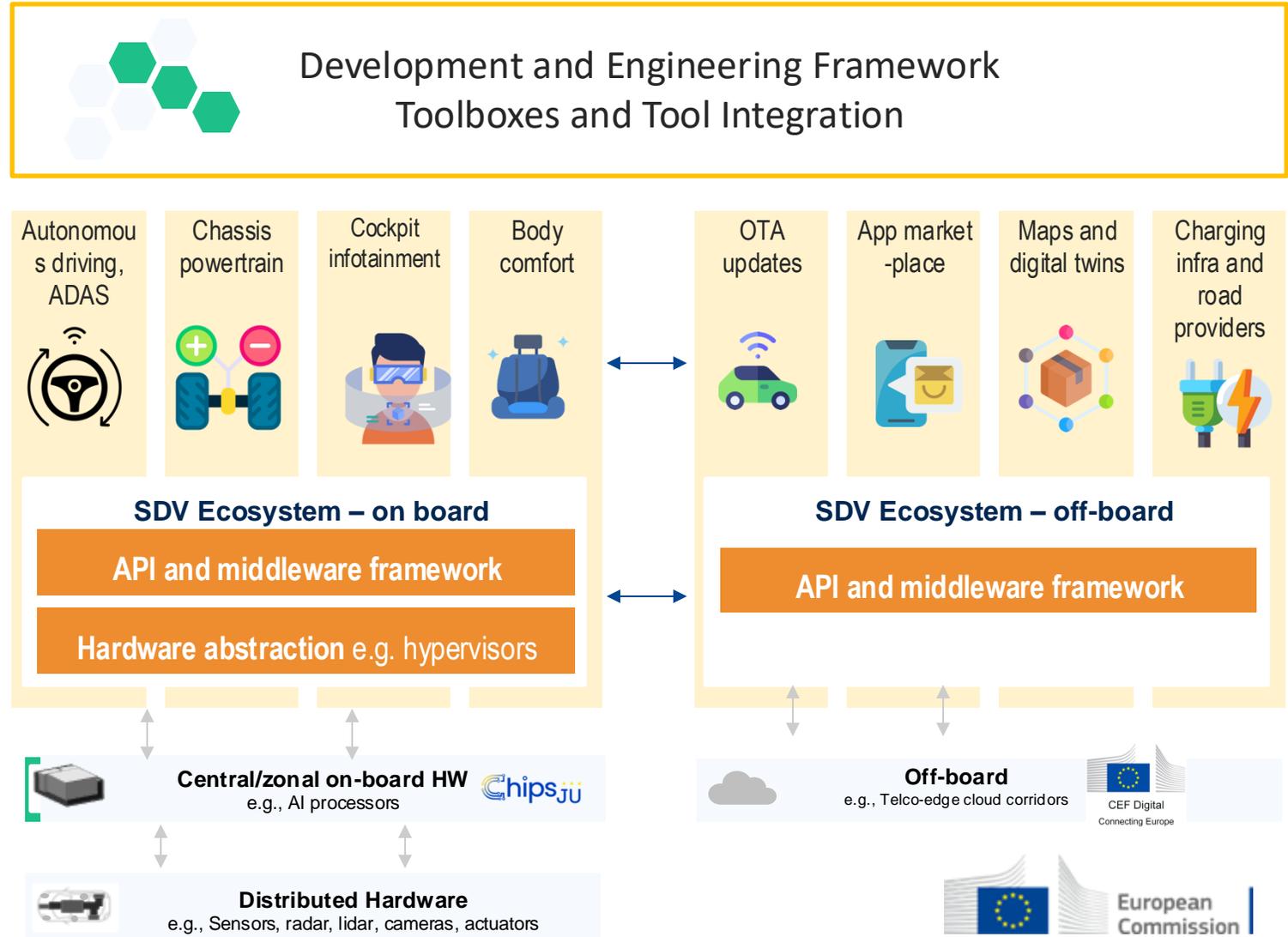
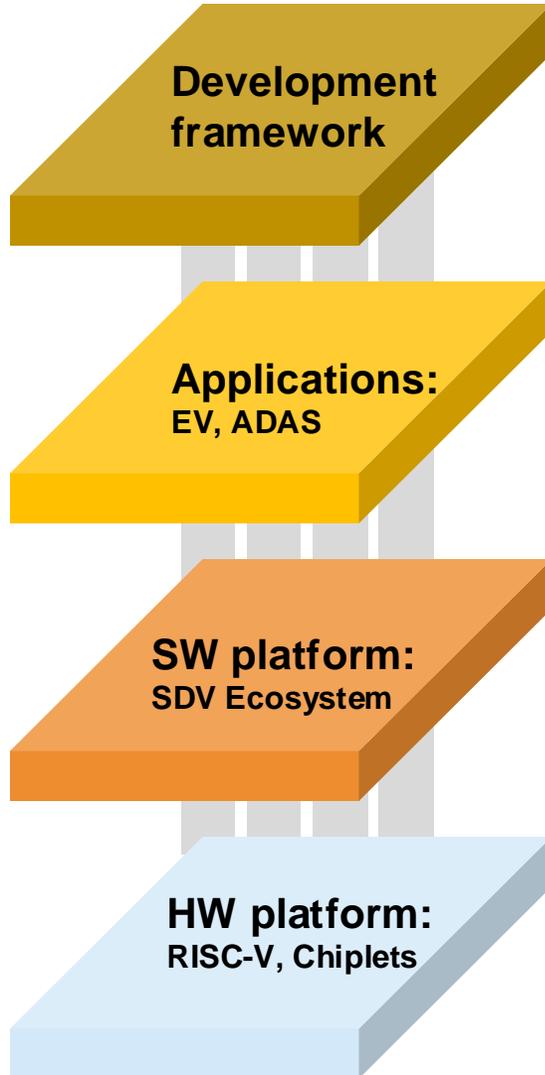
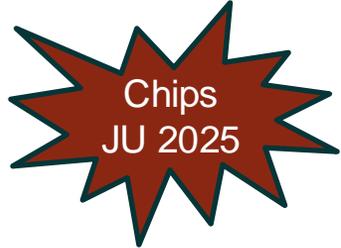
European Connected and Automated Vehicle Alliance



 **EU needs to build its own industrial capacities for the SW and IT hardware needed for clean, connected and automated vehicles.**

The Alliance will build on the preparatory work of the [European Vehicle of the Future Initiative](#), and [Horizon Europe automotive-related Partnerships](#)

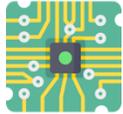
Overview of the Digital Vehicle ecosystem



Automotive Hardware

Automotive Action Plan

1



Advanced AI Accelerators

Development of AI-chips to reduce strategic dependencies but faces strategic dependencies on higher-end automotive application processors

2



RISC-V Automotive Hardware

Open, **royalty-free Instruction Set** for any stakeholder to build solutions and services on + RISC-V reference architecture and core IP.

3



Chiplets

Optimised Chips for automotive to meet **customised functions** to meet reliability, safety, security requirements



RISC-V Chip architecture
€54 m (15m EU)



RISC-V Domain specific ecosystems
€ >39m (12m EU)

RIGOLETTO
(in prep)

RISC-V

RISC-V for Automotive
€ >60m (20m EU)

RISC-V Automotive platform
€ >350m (80m EU)

Chiplets

European Automotive Chiplet system

€ >60m (20m EU)

Software-Defined Vehicles

Automotive Action Plan



HW/SW Abstraction

Abstraction layer ensures efficient integration of various hardware platforms while **maintaining compatibility** with the evolving software stack.

Modular and scalable to build SDV systems while **reducing dependencies** and vendor lock-in



Middleware and API framework

Middleware layer that bridges the hardware/OS and application layers.

Standardized middleware stack provides essential services such as **communication protocols, security mechanisms, and data management**



Automated DevOps tools

Integrated DevOps toolchain simplify adoption and use of new software layers, streamlining processes such as continuous **integration, testing, deployment, and monitoring.**

Use of **emerging AI tools** to **improve productivity** in the SW engineering process



Hardware Abstraction

€ 64m (18m EU)

Shift2SDV
(in prep)

Service oriented Framework

€ >60m (20m EU)

AI Tools

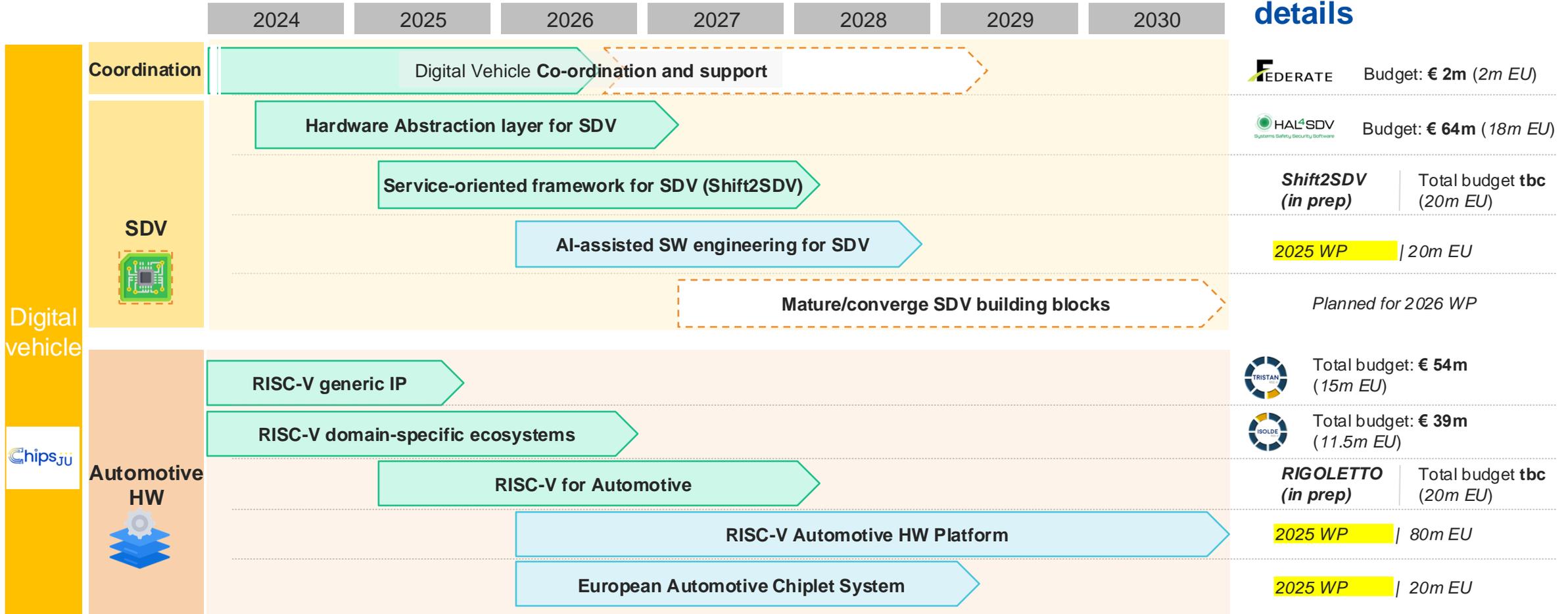
AI Dev Tools for SDV

€ >60m (20m EU)

Chips JU: Project Pipeline

 In WP/call planned
 Proposal for future WP – to be further discussed

Project details



Engage in an ecosystem approach

- Keep in mind Europe's strategic autonomy
- From OEMs and Tier1s to IDMs and tool providers
- Horizontal and vertical collaboration
- Platforms: ecosystem – market place – standards – piloting - ...
- Consider Open Source as a key vehicle for collaboration