

INFO SESSION FOR CHIPS JU CALLS ON AI CHIPS AND SYSTEMS FOR EU COMPUTE INFRASTRUCTURE



Friday, 3 July



10.00-12.00

www.chips-ju.europa.eu/Events



10:00-10:15	Welcome, Intro, policy context, call rationale (15')
10:15-10:25	Presentation of call A1 (AI chip & system developers' stream) (10')
10:25-10:35	Information for applicants (10')
10:35-11:05	Q&A (30')
11:05-11:15	Presentation of call B (GfP of Buyers stream) (10')
11:15-11:25	Information for applicants (10')
11:25-11:55	Q&A (30')
11:55-12:00	Closing remarks



AI Chips and Systems for EU Compute Infrastructure

Chips JU Work Programme 2026

INFORMATION WORKSHOP - 3 JULY 2026

Marco Ceccarelli

Lead Strategy Coordinator
Microelectronics & Photonics

DG CNECT

Why Europe must act now

AI compute has become a geopolitical asset - Europe must reduce its dependence

~100%

dependence on non-EU AI tech

Virtually every AI chip, rack and integrated system in European data centres comes from a handful of **non-EU** suppliers

Chokepoint

outside Europe's control

Export controls, supply disruptions, unilateral pricing and vendor lock-in create a structural **vulnerability** for the EU

Full stack

is what sovereignty takes

Competitive European capacity from **chip** design to complete rack **systems** – including hardware and software

The policy mandate



Competitiveness Compass & Chips Act - design cutting-edge AI chips, rebuild European semiconductor capacity

AI Continent Action Plan - AI Factories & Gigafactories as demand drivers for EU AI processors

Technology sovereignty package - Chips Act 2.0 + Cloud and AI Development Act - stimulate demand and industrial uptake, reduce structural dependencies across the AI compute stack

The policy framework is in place, this call is execution

The objective: bring EU supply and demand together

Hands-on testing and near-term deployment in European infrastructure

Connect European **chip designers and system developers** with strategic downstream users - **AI Factories, Cloud Service Providers and data centre operators** - for testing and optimisation of complete solutions on dedicated experimental partitions and common EU testbeds

END-TO-END AI COMPUTE SOLUTIONS

Complete software stack

Boards & racks

Networking

AI processors



Tested on common EU testbeds · deployed in EU infrastructure



High performance & energy efficiency



Scalable, full-stack solutions - chip to rack to software



Reduced reliance on non-EU providers

A Grand Challenge, run as an open competition

Performance-based selection: public funding follows demonstrated results

1 Open entry

Any EU chip design company with taped-out silicon can enter (no pre-selection of champions).

2 One common benchmark

A neutral EU evaluation platform: identical workloads, auditable KPIs, equal access for all.

3 Performance decides

Best-performing solutions earn the *AI Compute Excellence Label* - evidence, not promises.

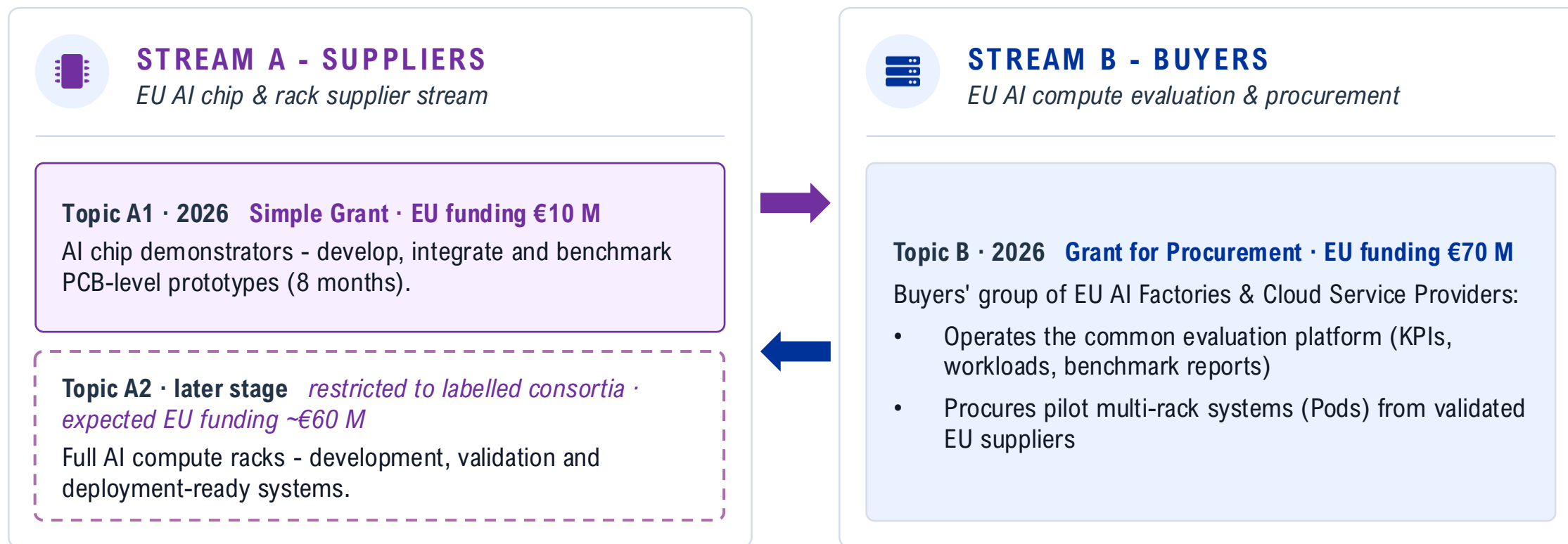
4 Winners scale up

Labelled consortia develop full racks and compete for real procurement by real buyers.

Winners are not picked on paper but by benchmarks

Selection on benchmark performance, energy efficiency, interoperability, software & operational readiness, price-performance and relevance for European sovereignty.

Two streams, one coordinated package

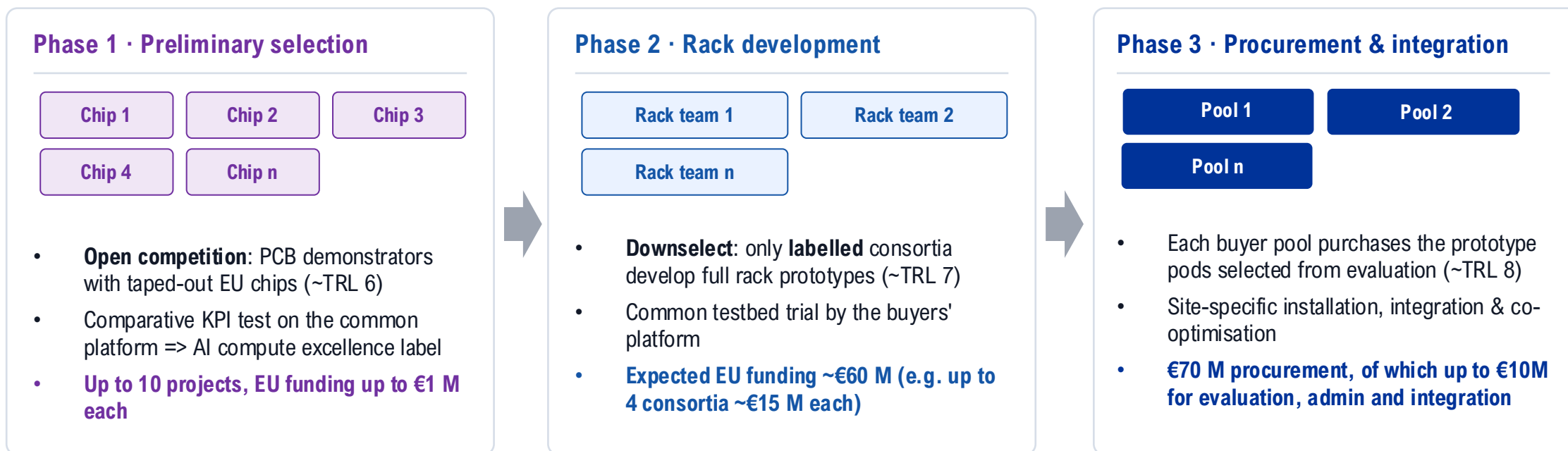


Operationally linked: Stream B provides the platform, workloads and validation evidence used to assess Stream A and then buys the winning systems.

Stream B is insulated from Stream A's technology risk: buyers procure only validated, benchmarked systems.

The funnel: competition at every stage

From many chip demonstrators to a few deployed Pods; merit decides who advances



EU funding across the package ≈ €140 M

Commensurate co-funding from JU Participating States expected

Total public funding up to ≈ €280 M

Where necessary, the above is to be complemented with participants' own private resources

How the two streams interlock

Calls A1 & B open 07 Jul 2026 · deadline 23 Sep 2026 · projects start ≈ M0 early 2027 · A2 call follows the Label

STREAM A Suppliers

A1: chip demonstrators

demonstrator

eval

A2: system development and delivery

rack development + benchmark

deployment & co-optimisation



AI Compute Excellence Label — gateway to A2

STREAM B Buyers

Phase 1: AI chip evaluation

Phase 2: AI system evaluation + tender preparation

Phase 3: procurement of Pods + pilots

Operationally linked streams

- To ensure seamless integration, the consortia of Stream A and B are expected to establish **mutual coordination arrangements** covering timelines, benchmark procedures, confidentiality and data sharing between suppliers and buyers
- The total expected timing of 42 months is indicative, the duration of each phase can be adapted based on agreements between the Stream A and B consortia

What's in it for you

Opportunities for every part of the European AI compute ecosystem



Chip design companies

- Funding to turn taped-out silicon into benchmarked demonstrators
- Comparative evidence on a neutral EU platform - visibility & credibility
- The Excellence Label: a springboard to rack funding, real purchase orders and private investment



System & integration partners

- Join supplier consortia for integration, software bring-up and benchmarking
- In A2: rack integration, cooling, networking, manufacturing readiness
- Position early - labelled consortia may expand with new partners for the rack phase



Buyers: AI Factories & CSPs

- You shape what gets built: KPIs, workloads and specs defined by you from day one
- **Merit-based awards:** spend your lot's budget on sovereign compute from the supplier that performs best in your environment
- First-of-a-kind EU systems with public funding, custom co-optimization, and a credible sovereignty offer for public-sector & regulated customers

- DEP Article 12(6) restrictions apply: entities controlled by third countries may be eligible with security guarantees.
- Where technically necessary, costs of non-EU subcontractors for specified services may be eligible.
- All data must be processed and stored in the EU.

Topic A1 · AI chip demonstrators (for suppliers)

Key facts

Simple Grant, co-funded with NFAs

€10 M indicative budget

~€1 M EU contribution per project

8 months: 6 development + 2 benchmark

Focused, limited-size consortia

EU funding rate: up to 50% of eligible costs

Launch 07 Jul 2026

Deadline 23 Sep 2026 · 17:00 CET

Who should apply

At least one EU-established fabless AI chip design company (owning the chip IP), optionally with system integrators / OEMs for demonstrator integration and software bring-up.

Entry requirements: real silicon, not slideware

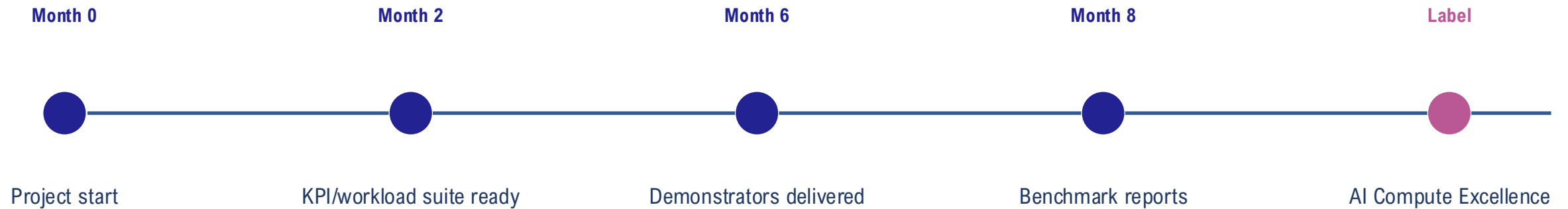
- Chip taped out at a commercial foundry; engineering samples within 3 months of the deadline
- PCB designed and in (or entering) production within the 6-month development phase
- Solution mature enough for bring-up, software enablement and benchmarking



The prize: the AI Compute Excellence Label

Awarded by the Chips JU to the best benchmark performers - the strict entry ticket to rack development under Topic A2

A1 selection: Benchmark evidence + Excellence Label



The Topic B consortium operates the evaluation platform and produces standardised benchmark reports. The Chips JU uses this evidence to identify the best-performing solutions and award the AI Compute Excellence Label.

Topic A2 - AI compute racks: a look ahead

Preliminary topic description, for information only - Topic A2 will be launched at a later stage

The next step for labelled consortia: from demonstrator to deployment-ready rack

Key facts (preliminary)

Restricted call: AI Compute Excellence Label holders only

Expected budget ~€60 M (to be confirmed)

Duration up to 32 months (development + benchmark + deployment)

EU-only fallback: 50% of eligible costs if national co-funding is not confirmed within 60 days

Launch at a later stage - proposals due within 1 month of Label notification

Who: your A1 consortium can be expanded if needed

Led by the **EU chip design company** owning the chip IP; may add **system integrators, OEMs, software, networking, cooling** and **manufacturing** partners for rack development and post-deployment support

What to build in Phase 2

- Scale the demonstrator into a **full AI compute rack**: chassis, power, cooling, networking and the complete software stack
- Rack-level orchestration, scheduling, monitoring & management; modular, open, interoperable interfaces
- Validate against the full KPI set on the common platform; manufacturing readiness, serviceability and a commercial offer template for the procurement

What follows

- **Benchmark evaluation** by the Topic B platform produces the standardised report used in procurement.
- Consortia selected as **suppliers** then **deliver, install and co-optimize** their racks with the buyers at the pilot sites

Prepare now: your A1 demonstrator, benchmark results and partner network will be the foundation of your A2 proposal



AI Chips and Systems for EU Compute Infrastructure

Topic B: the buyers' stream

How the two streams interlock

Calls A1 & B open 07 Jul 2026 · deadline 23 Sep 2026 · projects start ≈ M0 early 2027 · A2 call follows the Label

STREAM A Suppliers

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demonstrator

eval

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AI Compute Excellence Label — gateway to A2

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Operationally linked streams

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- The total expected timing of 42 months is indicative, the duration of each phase can be adapted based on agreements between the Stream A and B consortia

Topic B · Evaluation platform & procurement (for buyers)

Key facts

Grant for Procurement (GfP)

€70 M EU + national co-funding

Three phases, duration tbd (indicative 42m)

Max ~15 participants, from ≥ 3 Member States, EU/EEA-established

Most of the budget flows to suppliers via procurement

EU contribution: 25% for-profit / 35% SME / 50% public & non-profit

Launch 07 Jul 2026

Deadline 23 Sep 2026 · 17:00 CET

One buyers' group, two anchors



≥ 1 public AI/HPC operator

Contracting authority running an EU AI Factory — preferred Lead Procurer



≥ 2 EU Cloud Service Providers

Leading commercial procurement lots; neutrality safeguards required

Three phases, one mission



Common platform, KPIs & workloads; benchmark A1 demonstrators

Extend platform to rack level; benchmark A2 systems; prepare tenders

Innovation procurement of multi-rack pilot clusters; install & co-optimize

- *The platform is neutral: equal access, auditable KPIs, standardised benchmark reports*
- *The benchmark reports are factual, auditable **evidence**: they support **Chips JU selection** and later **buyer procurement** without replacing formal evaluation/procurement procedures*

Building the buyers' group: few pools, meaningful pilots

Concentrate, don't fragment - every installation must matter

Example with 3 pools



Each pool = one procurement lot = one meaningful pilot installation

Separate pools accommodate different requirements and competition among commercial buyers.

How to structure it well

- Keep the number of pools small (indicatively 2–3), each led by one lead buyer with committed members
- Concentrate the procurement budget: each pilot Pod must be of credible, meaningful scale, hence fragmentation across many small sites should be avoided
- Demonstrate neutrality vis-à-vis Stream A suppliers from the outset
- **Start now: align KPIs, workloads and site commitments before 23 September**

A compact, committed buyers' group is a selection advantage and delivers pilots that matter

Europe sovereign AI compute

Start building your consortium now!

- *Supplier teams need their system partners*
- *Buyers need to form pools and a joint group*

07 Jul 2026

Calls A1 & B open



23 Sep 2026 · 17:00

Submission deadline (Brussels time)

***Up next:** technical briefing on call modalities, eligibility, evaluation and how to apply.*



Thank you!

Marco Ceccarelli
Lead Strategy Coordinator
Microelectronics & Photonics
DG CNECT

How to prepare a strong proposal

Focus on readiness, evidence, and credible deployment pathways

For A1 supplier teams

- Show chip/PCB readiness and integration plan.
- Demonstrate software stack maturity and AI workload readiness.
- Define clear route from demonstrator to full rack.
- Document IP origin, supply dependencies and security controls.

For Stream B buyers

- Build a focused buyers group with clear lot leadership.
- Define neutral KPIs, workload suite and access procedures.
- Show testbed/site readiness and conflict-of-interest safeguards.
- Prepare procurement strategy for meaningful multi-rack deployments.

Proposal quality will be judged not only on technology ambition, but on operational credibility: can the solution be tested, compared, procured and deployed?

What can be benchmarked?

A common KPI suite reflects real operational requirements of buyers

Performance	throughput, latency, batch behaviour
Energy	perf/W, rack power envelope, cooling
Scalability	multi-chip, multi-node, networking fabrics
Software	drivers, compiler/runtime, framework support
Operations	orchestration, monitoring, reliability, serviceability
Value	price-performance and European deployment relevance



Benchmark reports become the common evidence layer: same workloads, controlled conditions, reproducible results.



DIGITAL-JU-CHIPS-2026-AI1-SG:

AI chip demonstrators for EU compute infrastructure

Information for applicants

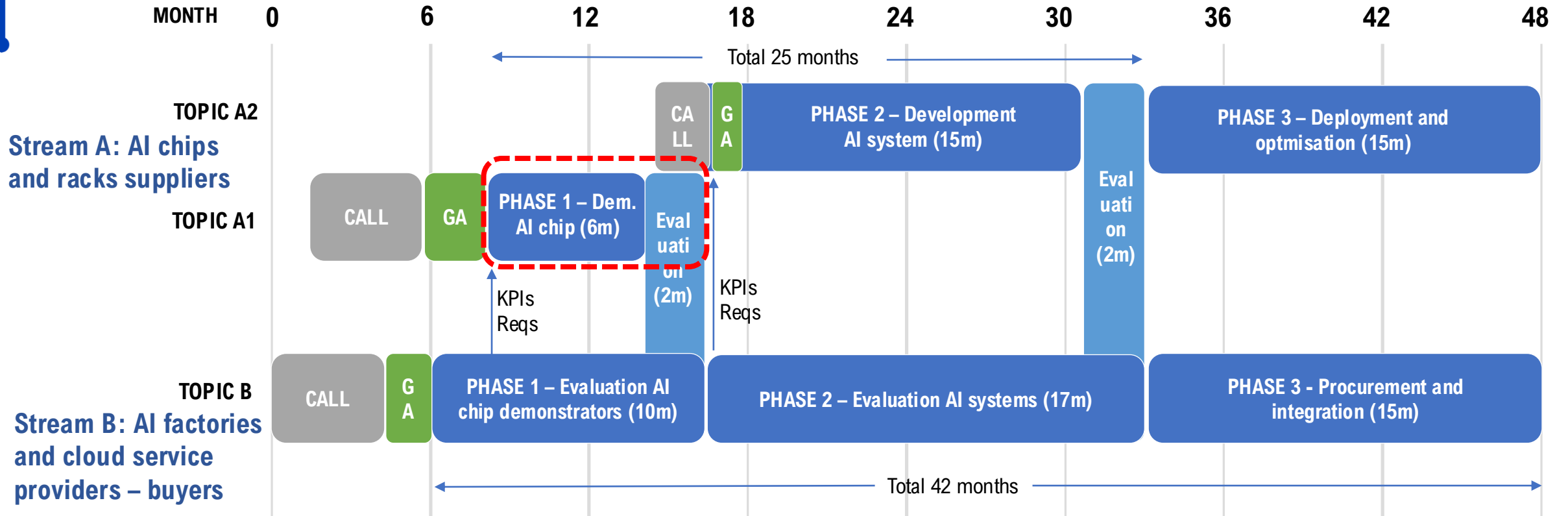
Huascar Espinoza, Chips JU Programme Officer

Info session, 3rd July 2026

EUROPEAN
PARTNERSHIP



Context: Stream A, Topic A1



Call documents

- Publication on **07 Jul 2026** with full package of call documents at:
<https://www.chips-ju.europa.eu/Open-and-Upcoming-Calls/>
- **Work Programme documents** can be already found at:
<https://www.chips-ju.europa.eu/MAWP/>
 - Multi-annual Work Programme 2023-2027
 - General Annexes to the Multi-annual Work Programme 2023-2027
 - Appendix 8 to the Work Programme - Chips for Europe initiative (CEI)

DIGITAL-JU- CHIPS-2026-AI1-SG

<i>Type of Action</i>	Simple Grant
<i>Indicative budget</i>	10 M€
<i>Expected EU contribution per project</i>	The JU estimates that an EU contribution of up to EUR 1 million would allow these outcomes to be addressed appropriately. Nonetheless, this does not preclude submission and selection of a proposal requesting different amounts.
<i>Mode</i>	Co-funded with the National Funding Authority (NFA) One stage Call with submission of Full Proposal (FPP)
<i>Call launch date</i>	07 Jul 2026
<i>Deadline FPP Phase</i>	23 Sep 2026 at 17:00 Brussels Time

Agenda

DIGITAL-JU- CHIPS-2026-AI1-SG

**Expected
outcome &
scope**

**Call
restrictions**

**Admissibility
& eligibility**

Funding

**Award
criteria**



DIGITAL-JU- CHIPS-2026-AI1-SG:

Expected outcome and scope (1/2)

- **Functional demonstrators** of working AI chip prototypes (PCB-level) designed by EU chip design companies and physically integrated with their system partners.
- **Validation** of AI chip solutions on a **common EU evaluation platform** (provided by the Topic B consortium), delivering comparative evidence on key performance indicators.
- **Demonstrable substantial EU added value** of the AI chip prototypes.
- A shortlist of European AI chip solutions awarded the **AI Compute Excellence label** and qualified to proceed to full rack development under Topic A2.



DIGITAL-JU- CHIPS-2026-AI1-SG:

Expected outcome and scope (2/2)

- Each proposal must include at least one **EU-established AI chip design company** as beneficiary, responsible for the AI chip design and associated IP. System integrators or OEMs may participate to support demonstrator integration.
- **Required technical maturity at submission:**
 - the AI chip has been **taped out at a commercial foundry**; engineering samples available or expected within the first 3 months after the submission deadline;
 - a **PCB hosting the chip** has been designed and is in production (or expected before the end of the development phase).
- **Duration: 8 months total:** 6 months development (chip, PCB, cooling, interconnect, software stack integration) + 2 months benchmark assessment on the Topic B common evaluation platform.
- **Final deliverable:** technical report covering architecture, energy efficiency, software maturity, origin of the core IP, and a **roadmap to a full AI compute rack at TRL 7**.



DIGITAL-JU- CHIPS-2026-AI1-SG: Admissibility & Eligibility (1/2)

- **Admissibility and Eligibility conditions:** Annex 2 “General DIGITAL EUROPE PROGRAMME conditions” to the Multiannual Work Programme 2023-2027 (General Annexes).
- **Page limits (FPP Phase):**
 - **Relevance:** 20 pages
 - **Implementation:** 40 pages
 - **Impact:** 20 pages
- Proposals with more pages are admissible and will be evaluated, but the pages in excess will not be considered for the evaluation.



DIGITAL-JU- CHIPS-2026-AI1-SG:

Admissibility & Eligibility (2/2)

- **Eligible applicants:** legal entities (public or private) established in eligible countries (EU Member States, EEA countries, countries associated to the Digital Europe Programme).
- **Specific eligibility: mandatory consortium composition:**
 - at least one **EU-established AI chip design company** as beneficiary, holding the IP for the proposed AI chip;
 - the consortium should remain focused and limited in size (system integrators or OEMs may participate as needed).
- The **AI Compute Excellence Label** awarded after benchmark assessment is a **strict eligibility condition** for participating in Topic A2 (rack development).

DIGITAL-JU-CHIPS-2026-AI1-SG:

Funding

- **Indicative budget:** EUR 10 million
- **Expected EU contribution per project:** up to EUR 1 million (submissions requesting different amounts are not precluded).
- **Mode:** Co-funded with the National Funding Authority (NFA). National co-funding from Participating States complements the EU contribution. Where national co-funding is not available, beneficiaries may proceed under EU-only funding and cover the remaining costs from own resources.
- **Reimbursement rates** (% of eligible cost according to DEP):
 - **For-profit organisation (not SME):** 50%
 - **SME (for-profit SME):** 50%
 - **University / Other (not for profit):** 50%

DIGITAL-JU- CHIPS-2026-AI1-SG:

Award criteria

➤ 3 award criteria: (sub-criteria details in the call documents)

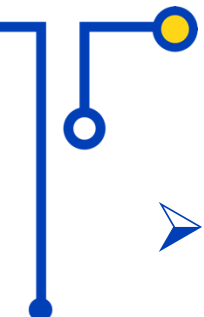
- Criterion 1 – Relevance
- Criterion 2 – Implementation
- Criterion 3 – Impact

➤ Scores:

<u>Criteria</u>	<u>Range</u>	<u>Weight (**)</u>	<u>Threshold (*)</u>
Relevance	<u>0-5</u>	<u>1</u>	<u>3</u>
Implementation	<u>0-5</u>	<u>1</u>	<u>3</u>
Impact	<u>0-5</u>	<u>1</u>	<u>3</u>
Total	<u>0-15</u>		<u>10</u>

(*) threshold applies to unweighted score.

(**) the weight is only used to establish the ranking of the proposals.



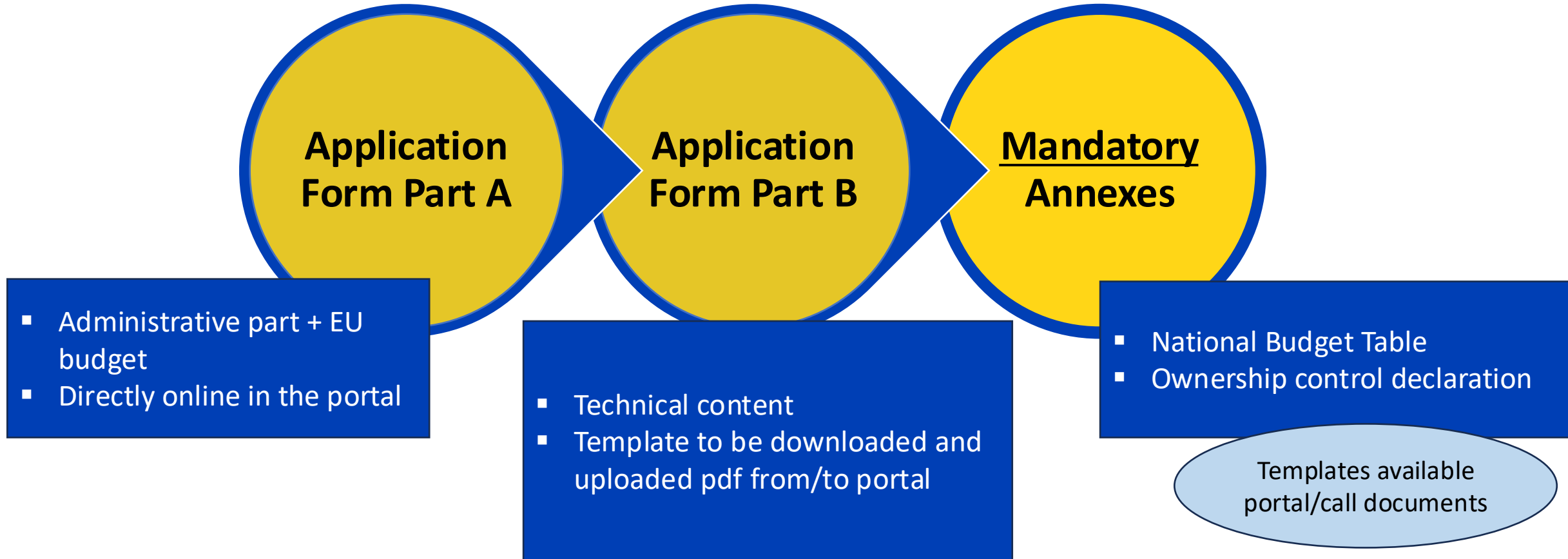
DIGITAL-JU- CHIPS-2026-AI1-SG:

Call restrictions

- Call is restricted on the basis of **Article 12(6)** of the Regulation (EU) 2021/6941
- Legal entities established in associated countries and legal entities that are established in the Union but are controlled from third countries may be eligible to participate only if they comply with certain requirements or conditions. Please see Annex IV of Appendix 4 of the Chips JU Work Programme for more information.
- All design and test data, builds, logs and KPI results must be processed and stored in the EU
- Ownership Control Declaration form to be filled in per applicant
 - Template to be download from the call documents

DIGITAL-JU- CHIPS-2026-AI1-SG:

Application steps



DIGITAL-JU- CHIPS-2026-AI1-SG:

Part B & Mandatory Annexes

Part B and Annexes

In this section you may upload the technical annex of the proposal (in PDF format only) and any other requested attachments. ⓘ

Part B *



Upload

List of previous projects



Upload

Ownership control declaration



Upload

Other annexes



Upload

Application Form Part B

NOT needed

Consortia Ownership Control Declaration
(per applicant – merged pdf if needed)

National budget table

All Annexes are mandatory!!!
(All annexes in pdf)

Further information

- Chips JU website (<https://www.chips-ju.europa.eu/Open-and-Upcoming-Calls/>) for further information on the calls.
- Calls@chips-ju.europa.eu for specific questions related to call documents.
- IT Helpdesk for questions regarding submission issues:
<https://ec.europa.eu/info/funding-tenders/opportunities/portal/screen/support/helpdesks/contact-form>
- EC online manual:
https://ec.europa.eu/info/funding-tenders/opportunities/docs/2021-2027/common/guidance/om_en.pdf
- For questions about European research, you can contact the Research Enquiry Service via the webform :
https://research-and-innovation.ec.europa.eu/contact-us/research-enquiry-service_en



Thank you for your attention!





DIGITAL-JU-CHIPS-2026-AI-GfP: AI compute evaluation and deployment platform for EU infrastructure

Information for applicants

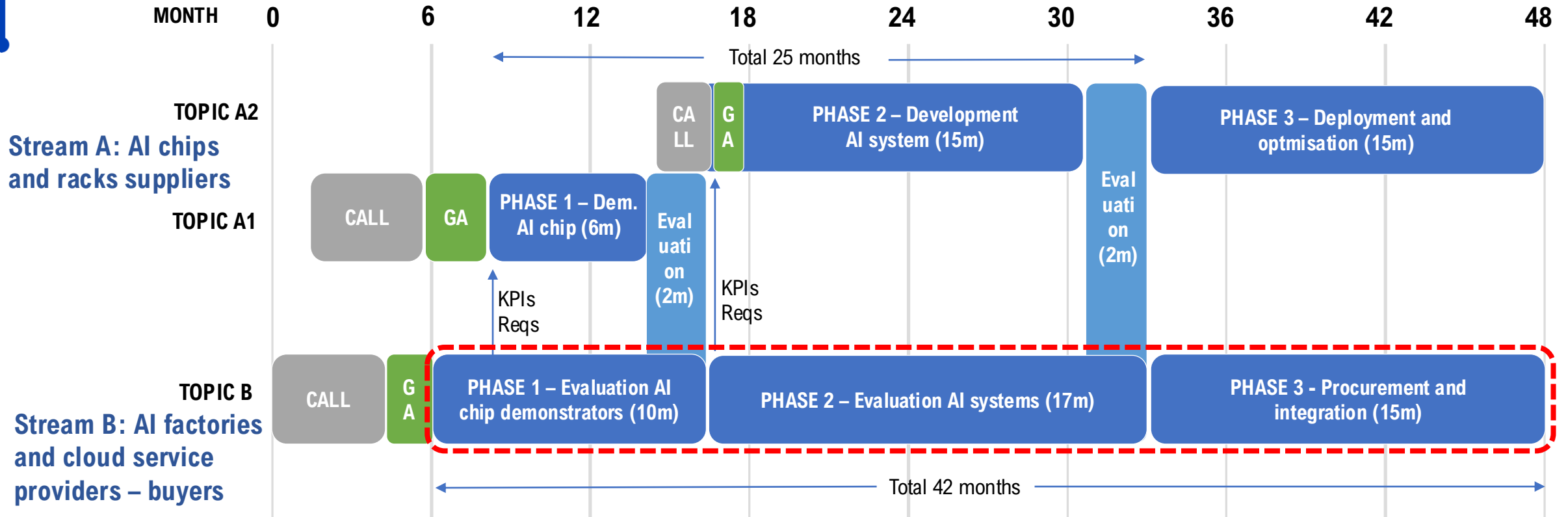
Huascar Espinoza, Chips JU Programme Officer

Info session, 3rd July 2026

EUROPEAN
PARTNERSHIP



Context: Stream B, Topic B



Call documents

- Publication on **07 Jul 2026** with full package of call documents at:
<https://www.chips-ju.europa.eu/Open-and-Upcoming-Calls/>
- **Work Programme documents** can be already found at:
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 - General Annexes to the Multi-annual Work Programme 2023-2027
 - Appendix 8 to the Work Programme - Chips for Europe initiative (CEI)

DIGITAL-JU-CHIPS-2026-AI-GfP

<i>Type of Action</i>	Grant for Procurement
<i>Indicative EU budget</i>	EUR 70 million
<i>Expected EU contribution per project</i>	The Chips JU estimates that an EU contribution of EUR 70 million would allow these outcomes to be addressed appropriately. Co-funding from Participating States is expected to complement the EU contribution.
<i>Mode</i>	Co-funded with the National Funding Authority (NFA) One stage Call with submission of Full Proposal (FPP)
<i>Call launch date</i>	07 Jul 2026
<i>Deadline FPP Phase</i>	23 Sep 2026 at 17:00 Brussels Time

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DIGITAL-JU-CHIPS-2026-AI-GfP:

Expected outcome and scope (1/2)

- A **common EU evaluation platform** for AI chips and racks, with transparent KPIs, workloads and benchmark reports used for Chips JU selection and procurement decisions.
- **First pilot deployments** of European AI compute systems validated for EU sovereign partitions of AI infrastructure.
- **Structured collaboration** between buyers and suppliers, supporting deployment, integration and optimisation of European AI compute solutions.
- Stronger **EU demand** for European AI chips and racks, helping to create a market for deployment-ready solutions.
- Increased **EU technological sovereignty** across the AI compute stack for European datacentres.



DIGITAL-JU-CHIPS-2026-AI-GfP:

Expected outcome and scope (2/2)

- **Total duration: 42 months**, organised in 3 operationally linked phases:
- **Phase 1 (months 0–10):** common KPI, testbed preparation and benchmark evaluation of Topic A1 chip demonstrators. Key milestones: M2 KPI/workload suite v1.0; M4 platform operational; M8 evaluation; M10 benchmark reports, AI Compute Excellence Label assignment.
- **Phase 2 (months 11–27):** KPI/testbed extension and benchmark evaluation of full AI systems (Topic A2 racks). M22 platform operational for racks and Phase 3 tender documents finalised; M27 procurement launch.
- **Phase 3 (months 27–42):** innovation procurement of pilot AI compute racks across multiple lots tailored to **AI Factory pool** (public contracting authorities) and **CSP pool** (EU Cloud Service Providers).
- Procurement targets **multi-rack clusters (Pods, indicatively 4+ interconnected racks)** rather than isolated single-rack deployments.



DIGITAL-JU-CHIPS-2026-AI-GfP: Admissibility

- **Admissibility conditions** are described in Annex 2 “General DIGITAL EUROPE PROGRAMME conditions” to the Multiannual Work Programme 2023-2027 (General Annexes).
- **Page limits (FPP Phase):**
 - **Relevance:** 20 pages
 - **Implementation:** 40 pages
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- Proposals with more pages are admissible and will be evaluated, but the pages in excess will not be considered.

DIGITAL-JU-CHIPS-2026-AI-GfP: Eligibility

- Eligibility conditions are described in Annex 2 “General DIGITAL EUROPE PROGRAMME conditions” to the Multiannual Work Programme 2023-2027 (General Annexes).
- Specific eligibility conditions:
 - The consortium should consist of beneficiaries from at least three different Member States or EEA countries.
 - Maximum number of participants 15 (indicative; the consortium should remain focused, with a limited number of core buyer beneficiaries leading the procurement lots)
- Each proposal should be submitted by a consortium of EU-established legal entities forming a buyers' group, including:
 - at least one contracting authority or contracting entity within the meaning of Directives 2014/24/EU or 2014/25/EU, operating an EU AI Factory or comparable EU public AI/HPC facility, leading a procurement lot in Phase 3;
 - at least two EU-established Cloud Service Providers, leading one or more procurement lots in Phase 3

DIGITAL-JU-CHIPS-2026-AI-GfP: Funding

- Indicative EU budget: EUR 70 million
- Additional considerations:
 - A limited share of the public contribution is allocated to Phase 1 and Phase 2, covering the evaluation platform, the workload and KPI work, the validation services and the procurement preparation. The majority of the public contribution is reserved for Phase 3, flowing mainly through procurement contracts to suppliers under the lots, together with installation and optimisation.
 - Up to EUR 10 million of the action budget may be reserved for procurement preparation and management, neutral benchmarking, testing and benchmark evaluation, legal compliance, and the Phase 1 and Phase 2 evaluation services to the Chips JU.
- Reimbursement rates:

Reimbursement rates as percentages of the eligible cost according to DEP.

Type of beneficiary	Maximum EU Contribution as % of the Eligible Cost according to DEP (*)
Undertakings carrying out economic activities (for profit, not SMEs)	25 %
Small and Medium Enterprises (for profit)	35 %
Public bodies and not-for-profit entities carrying out non-economic public-interest activities	50 %

(*) beneficiaries may ask for a lower contribution.

DIGITAL-JU-CHIPS-2026-AI-GfP:

Award criteria

➤ Please refer to Annex 2 “General DIGITAL EUROPE PROGRAMME conditions” to the multiannual work programme 2023-2027 (General Annexes).

- Criterion 1 – Relevance
- Criterion 2 – Implementation
- Criterion 3 – Impact

➤ Scores:

The scores will be given with a resolution of one decimal.

Criteria	Range	Weight (**)	Threshold (*)
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Implementation	0-5	1	3
Total	0-15		10

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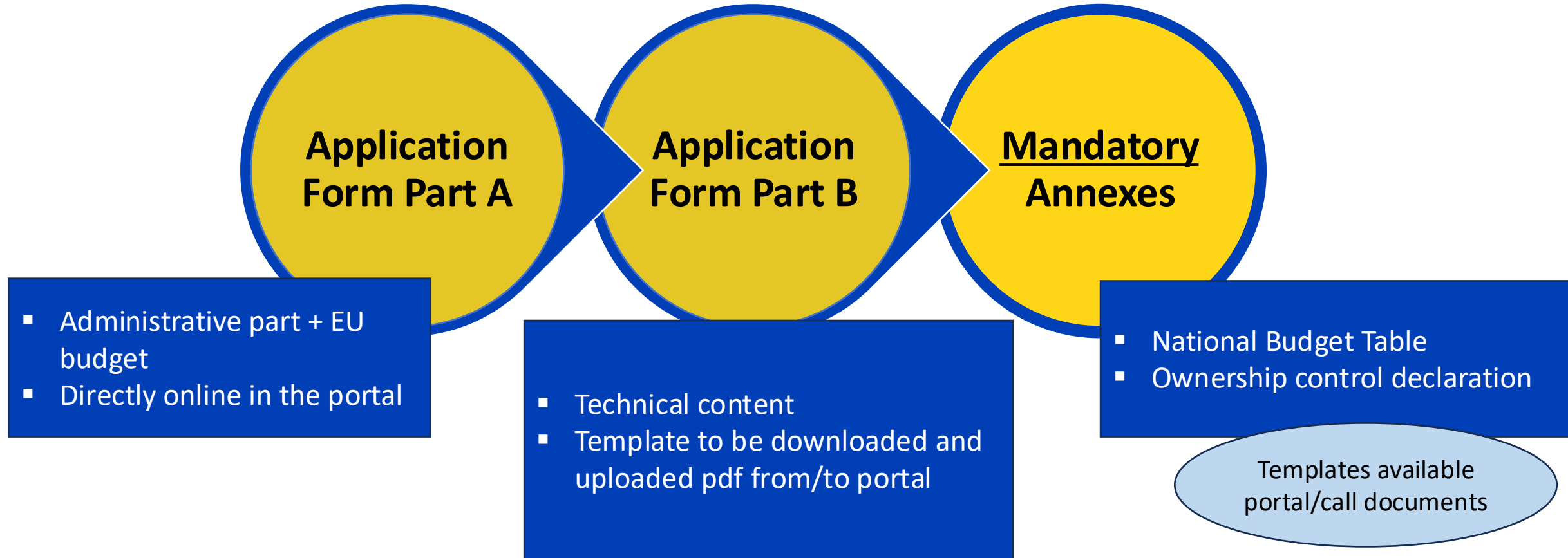
DIGITAL-JU-CHIPS-2026-AI-GfP:

Call restrictions

- Subject to participation restrictions for the protection of European digital infrastructures, communication and information systems, and related supply chains, as described in Annex 4 to the multiannual work programme 2023-2027 (General Annexes).
- Legal entities that are established in EU Member States, EEA countries (Iceland, Liechtenstein, and Norway), but are controlled from third countries may only participate on the condition that they guarantee the protection of the essential security interests of the Union and the Member States and that they ensure the protection of classified documents information. Where applicable, security guarantees need to be provided after proposal selection.
- Non-EU subcontractors: eligible for specialised services not sufficiently available in the EU, if strictly necessary and identified in the proposal. Same safeguards apply to Phase 3 procurement contractors. See WP §5.2.3.
- Data localisation: all design and test data, builds, logs and KPI results must be processed and stored in the EU. External access via EU-audited remote controls only.

DIGITAL-JU-CHIPS-2026-AI-GfP:

Application steps



DIGITAL-JU-CHIPS-2026-AI-GfP:

Part B & Mandatory Annexes

Part B and Annexes

In this section you may upload the technical annex of the proposal (in PDF format only) and any other requested attachments. ⓘ

Part B *



Upload

List of previous projects



Upload

Ownership control declaration



Upload

Other annexes



Upload

Application Form Part B

NOT needed

Consortia Ownership Control Declaration
(per applicant – merged pdf if needed)

National budget table

All Annexes are mandatory!!!
(All annexes in pdf)

Further information

- Chips JU website (<https://www.chips-ju.europa.eu/Open-and-Upcoming-Calls/>) for further information on the calls.
- Calls@chips-ju.europa.eu for specific questions related to call documents.
- IT Helpdesk for questions regarding submission issues:
<https://ec.europa.eu/info/funding-tenders/opportunities/portal/screen/support/helpdesks/contact-form>
- EC online manual:
https://ec.europa.eu/info/funding-tenders/opportunities/docs/2021-2027/common/guidance/om_en.pdf
- For questions about European research, you can contact the Research Enquiry Service via the webform :
https://research-and-innovation.ec.europa.eu/contact-us/research-enquiry-service_en



Thank you for your attention!

